

DFW

VP083

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Yun Shon Low, et al.

Group Art Unit: 2674

Serial No.: 10/621,596

Examiner: Patrick N. Edouard

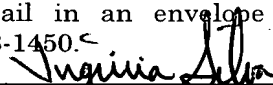
Filed: July 17, 2003

Title: System and Method for Displaying a Parallel Panel Simultaneously  
With an RGB Panel

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence, is on this date, being deposited with the United States Postal Service with sufficient postage as "First Class" mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date: April 13, 2005

  
Virginia Silva

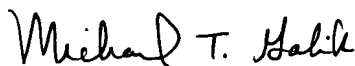
**STATUS REQUEST**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

It is respectfully requested that Applicant(s) attorneys be advised of the current status of the above-identified application.

Respectfully submitted,



Michael T. Gabrik  
Registration No. 32,896

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Date: April 13, 2005